



CN5	
1	VDD
2	NRST
3	SWCLK
4	SWDIO
5	SWO
6	GND

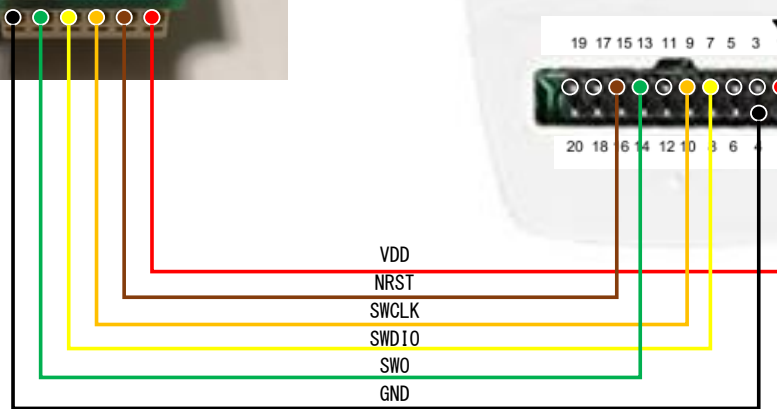


Table 4. JTAG/SWD cable connections

Pin no.	ST-LINK/V2 connector (CN3)	ST-LINK/V2 function	Target connection (JTAG)	Target connection (SWD)
1	VAPP	Target VCC	MCU VDD ⁽¹⁾	MCU VDD ⁽¹⁾
2	TRST	JTAG TRST	JNTRST	GND ⁽²⁾
3	GND ⁽³⁾	GND ⁽³⁾	GND ⁽³⁾⁽⁴⁾	GND ⁽³⁾⁽⁴⁾
4	TDI	JTAG TDO	JTDI	GND ⁽²⁾
5	GND ⁽³⁾	GND ⁽³⁾	GND ⁽³⁾⁽⁴⁾	GND ⁽³⁾⁽⁴⁾
6	TMS_SWDIO	JTAG TMS, SW IO	JTMS	SWDIO
7	GND ⁽³⁾	GND ⁽³⁾	GND ⁽³⁾⁽⁴⁾	GND ⁽³⁾⁽⁴⁾
8	TCK_SWCLK	JTAG TCK, SW CLK	JTCK	SWCLK
9	GND ⁽⁵⁾	GND ⁽⁵⁾	GND ⁽⁴⁾⁽⁵⁾	GND ⁽⁴⁾⁽⁵⁾
10	Not connected	Not connected	Not connected	Not connected
11	GND	GND	GND ⁽⁴⁾	GND ⁽⁴⁾
12	TDO_SWO	JTAG TDI, SWO	JTDO	TRACESWO ⁽⁶⁾
13	GND ⁽⁵⁾	GND ⁽⁵⁾	GND ⁽⁴⁾⁽⁵⁾	GND ⁽⁴⁾⁽⁵⁾
14	NRST	NRST	NRST	NRST
15	GND ⁽³⁾	GND ⁽³⁾	GND ⁽³⁾⁽⁴⁾	GND ⁽³⁾⁽⁴⁾
16	Not connected	Not connected	Not connected	Not connected
17	GND	GND	GND ⁽⁴⁾	GND ⁽⁴⁾
18	VDD ⁽³⁾	VDD (3.3 V) ⁽³⁾	Not connected	Not connected
19	GND	GND	GND ⁽⁴⁾	GND ⁽⁴⁾
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